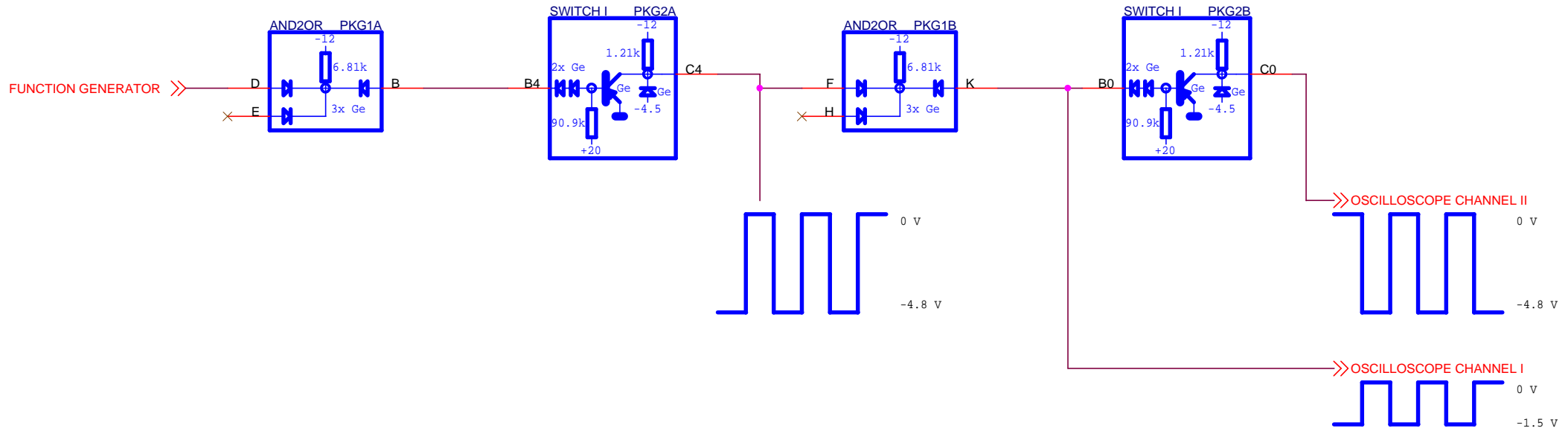


SWITCH I TEST CIRCUIT



Note:
 The signal swing of the output of an AND/OR gate is always limited by the input circuitry of the following active element to roughly -1.5 Volts